Electronic Acknowledgement Receipt FES ID: 1179317 Application Number: 10016449 Confirmation Number: 5286 Method and system for use of a field programmable gate array (FPGA) Title of Invention: function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC First Named Inventor: Robert Thomas Bailis Customer Number: 47052 Filer: Kelvin M. Vivian/Jinny Nguyen Filer Authorized By: Kelvin M. Vivian Attorney Docket Number: BPS920010127US1 Receipt Date: 30-AUG-2006 Filing Date: 10-DEC-2001 Time Stamp: 17:43:39 Application Type: Utility International Application Number:

Payment information:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Appeal Brief Filed	RPS920010127US1AppealB rief.pdf	247973	no	13
Warnings:					
Information:					
2	Transmittal letter	RPS920010127US1Transm. pdf	236945	no	1
Warnings:		·			
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	8266	no	2
Warnings:					
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.